

Doc. Number: MT185GW01 V.4-DR4-26

Tentative Specification

Preliminary Specification

Approval Specification

MODEL NO.: MT185GW01 SUFFIX: V.4

Customer:	
APPROVED BY	SIGNATURE
Name/ Note	
Please return 1 copy for y signature and comments.	our confirmation with your

Approved By	Checked By	Prepared By
And 19 was	7-19200	武马没 86-10

Version 1.0

6 August 2010

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PRODUCT SPECIFICATION

1. GENERAL DESCRIPTION

1.1 OVERVIEW

MT185GW01 V.4 is a 18.5" TFT Liquid Crystal Display module with WLED Backlight unit and 30 pins 2ch-LVDS interface. This module supports 1366*768 HD mode and can display up to 16.7M colors. The converter module for Backlight is not built in.

1.2 GENERAL SPECIFICATIONS

Item	Specification	Unit	Note
Screen Size	18.5" real diagonal		
Driver Element	a-si TFT active matrix	-	-
Pixel Number	1366x R.G.B. x 768	pixel	-
Pixel Pitch	0.3(H) X 0.3 (V)	mm	-
Pixel Arrangement	RGB vertical stripe	-	-
Display Colors	16.7M	color	-
Transmissive Mode	Normally white	-	-
Surface Treatment	AG type, 3H hard coating, Haze 25%	_	-
Luminance, White	250nit(Typ.)	Cd/m2	-
Power Consumption	Total 10.98W (Max.) @ cell 2.9W (Max.), BL8.08 W (Max.)	/	(1)
TCO 5.0	TCO 5.0 compliance		
Color Saturation	70% NTSC		

Note (1) The specified power consumption: Total= cell (reference 4.3.1)+BL (reference 4.3.3)

2. MECHANICAL SPECIFICATIONS

It	Item		Тур.	Max.	Unit	Note
	Horizontal (H)	429.87	430.37	430.87	mm	
Module Size	Vertical (V)	254.1	254.6	255.1	mm	(1)
	Thickness (T)	-	9.8	10.3	mm	
Bezel Area	Horizontal	413.4	413.6	413.8	mm	
Dezei Alea	Vertical	234.0	234.2	234.4	mm	
Active Area	Horizontal	-	409.8	-	mm	
Active Area	Vertical	ı	230.4	-	mm	
Weight		-	1600	1800	g	

Note (1) Please refer to the attached drawings for more information of front and back outline dimensions.

3. ABSOLUTE MAXIMUM RATINGS

3.1 ABSOLUTE RATINGS OF ENVIRONMENT

Item	Symbol	Va	lue	Unit	Note
item	Symbol	Min.	Max.	Offic	Note
Storage Temperature	TST	-20	60	°C	(1)
Operating Ambient Temperature	TOP	0	50	°C	(1), (2)

Note (1)

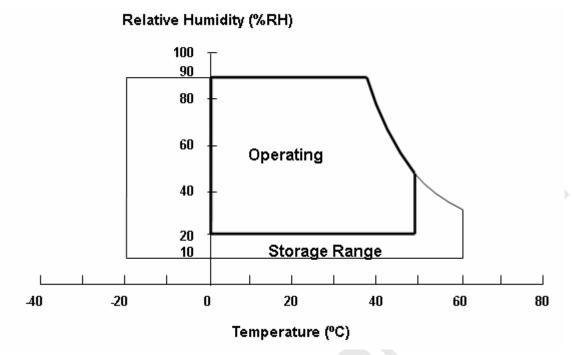
- (a) 90 %RH Max. (Ta <= 40 °C).
- (b) Wet-bulb temperature should be 39 °C Max. (Ta > 40 °C).
- (c) No condensation.

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Note (2) The temperature of panel surface should be 0 °C min. and 60 °C max.



3.2 ELECTRICAL ABSOLUTE RATINGS

3.2.1 TFT LCD MODULE

Item	Symbol	Va	lue	Unit	Note
itom	Cymbol	Min.	Max.	OTIL	11010
Power Supply Voltage	vccs	-0.3	6.0	V	(1)
Logic Input Voltage	V _{IN}	-0.3	3.6	V	(1)

3.2.3 BACKLIGHT UNIT

Item	Item Symbol		Value			Note	
iteiii	Syllibol	Min.	Тур	Max.	Unit	Note	
LED Forward Current Per Input Pin	F	0	25	27.5	mA	(1), (2)	
LED Reverse Voltage Per Input Pin	V_R			70	V	Duty=100%	
LED Pulse Forward Current Per Input Pin	l _P			80	mA	(1), (2) Pulse Width≦10msec. and Duty≦10%	

Note (1) Permanent damage to the device may occur if maximum values are exceeded. Function operation should be restricted to the conditions described under Normal Operating Conditions.

Note (2) Specified values are for input pin of LED light bar at Ta=25±2 °C (Refer to 4.3.3 and 4.3.4 for further information).

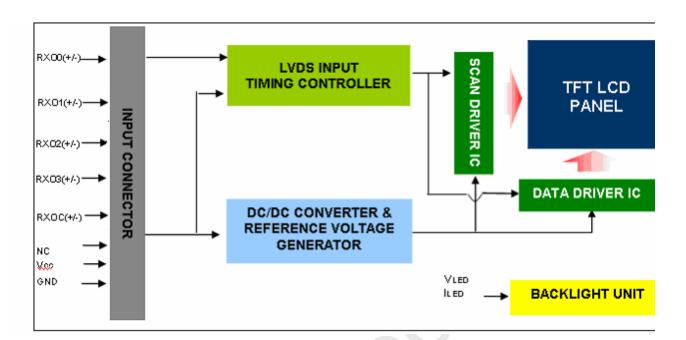
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4. ELECTRICAL SPECIFICATIONS

4.1 FUNCTION BLOCK DIAGRAM



4.2. INTERFACE CONNECTIONS

PIN ASSIGNMENT

PIN ASSI	GNMENI	
Pin	Name	Description
1	NC	Not connection, this pin should be open
2	NC	Not connection, this pin should be open
3	NC	Not connection, this pin should be open
4	GND	Ground
5	RO0-	Negative LVDS differential data input. Channel O0 (odd)
6	RO0+	Positive LVDS differential data input. Channel O0 (odd)
7	GND	Ground
8	RX1-	Negative LVDS differential data input. Channel O1 (odd)
9	RX1+	Positive LVDS differential data input. Channel O1 (odd)
10	GND	Ground
11	RX2-	Negative LVDS differential data input. Channel O2 (odd)
12	RX2+	Positive LVDS differential data input. Channel O2 (odd)
13	GND	GND
14	RXCLK-	Negative LVDS differential clock input. (odd)
15	RXCLK+	Positive LVDS differential clock input. (odd)
16	GND	Ground
17	RX3-	Negative LVDS differential data input. Channel O3 (odd)
18	RX3+	Positive LVDS differential data input. Channel O3 (odd)
19	GND	Ground
20	NC	Not connection, this pin should be open
21	NC	Not connection, this pin should be open
22	NC	Not connection, this pin should be open
23	GND	Ground
24	GND	Ground
25	GND	Ground
26	Vcc	+5.0V power supply

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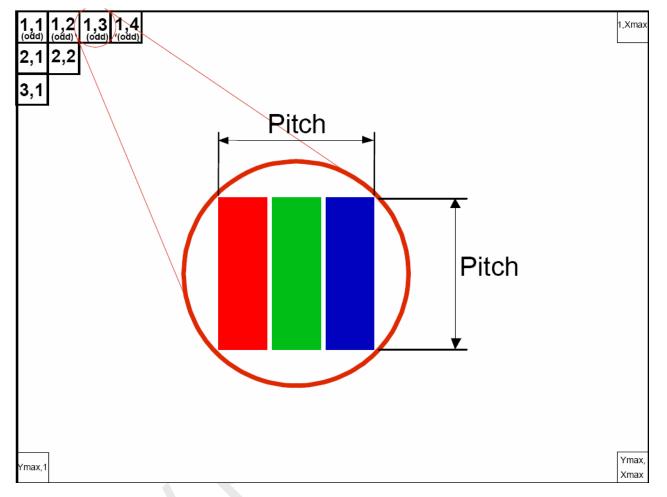
PRODUCT SPECIFICATION

C		INNOLIIX	
	27	Vcc	+5.0V power supply
	28	Vcc	+5.0V power supply
	29	Vcc	+5.0V power supply
	30	Vcc	+5.0V power supply

Note (1) Connector Part No.:

GS23302-0011R-7F (FOXCONN)

Note (2) The pixel is odd.



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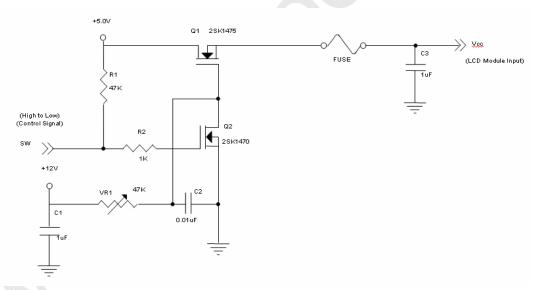
4.3 ELECTRICAL CHARACTERISTICS

4.3.1 LCD ELETRONICS SPECIFICATION

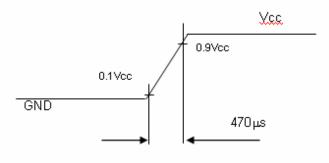
Parame	ator	Symbol		Value		Unit	Note
raiaile		Symbol	Min.	Тур.	Max.	Offic	NOLE
Power Supply	Vcc	4.5	5	5.5	V	-	
Ripple Vo	ltage	V_{RP}	-	ı	0.15	V	-
Rush Cu	rrent	I _{RUSH}	-	1.6	3	Α	(2)
	White		-	500	700	mA	(3)a
Power Supply Current	Black		-	700	1000	mA	(3)b
	Vertical Stripe		-	700	1000	mA	(3)c
Power Cons	umption	PLCD	-	-	2.90	Watt	(4)
LVDS differential	input voltage	Vid	100	ı	600	mV	
LVDS common in	Vic	1.0	1.2	1.4	٧		
Logic High Input Voltage			-	-	100	mV	
Logic Low Inpo	ut Voltage	VIL	-100	-	-	mV	

Note (1) The ambient temperature is $Ta = 25 \pm 2$ °C.

Note (2) Measurement Conditions:



Vcc rising time is 470µs

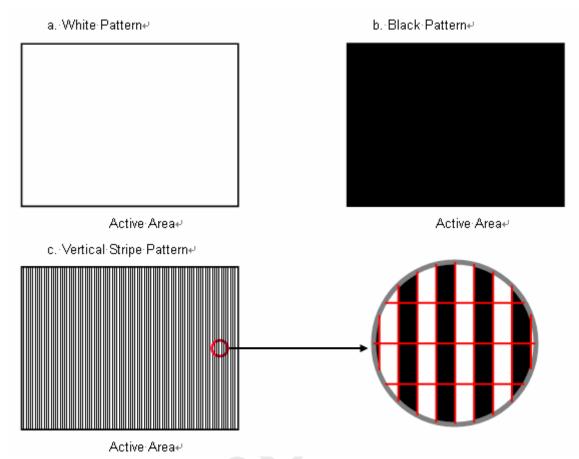


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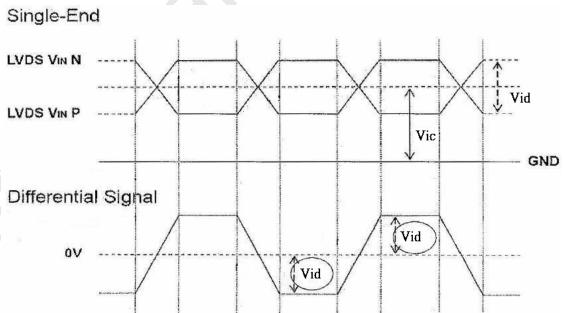


Note (3) The specified power supply current is under the conditions at Vcc = 5.0 V, Ta = 25 ± 2 °C, Fr = 60Hz, whereas a power dissipation check pattern below is displayed.



Note (4) The power consumption is specified at the pattern with the maximum current.

Note (5) VID waveform condition

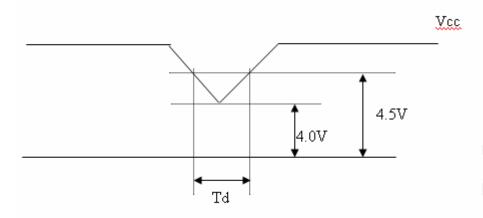


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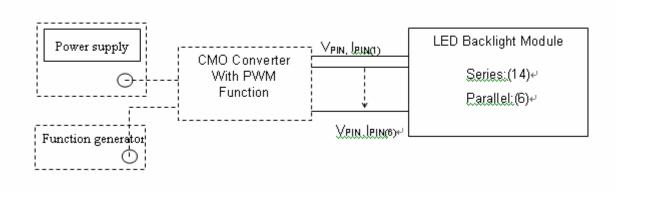
4.3.2 Vcc Power Dip Condition



4.3.3 BACKLIGHT UNIT (LED matrix is 14S6P)

Parameter	Symbol		Value		Unit	Note
r arameter	Syllibol	Min.	Тур.	Max.	OTIL	Note
LED Light Bar Input Voltage Per Input Pin	VPIN	-	44.8	49	٧	(1), Duty=100%, IPIN=25mA
LED Light Bar Current Per Input Pin	IPIN		25	27.5	mA	(1), (2) Duty=100%
LED Life Time	LLED	30000			Hrs	(3)
Power Consumption	PBL		6.72	8.08	W	(1) Duty=100%, IPIN=25mA

- Note (1) LED light bar input voltage and current are measured by utilizing a true RMS multimeter as shown below:
- Note (2) PBL = IPIN × VPIN × (6) input pins, LED light bar circuit is (14)Series, (6)Parallel.
- Note (3) The lifetime of LED is defined as the time when LED packages continue to operate under the conditions at Ta = 25 ±2 °C and I= (25)mA (per chip) until the brightness becomes ≤ 50% of its original value.



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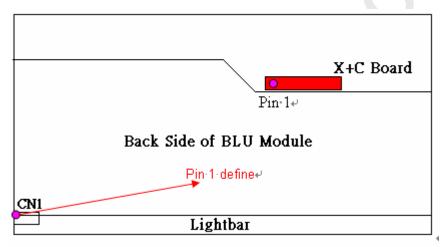
4.3.4 LIGHTBAR Connector Pin Assignment

Connector: 7083K-F10N-01L, (Entery) or FF04-404-103A,(FCN)

CN1

Pin number	Description
1	Cathode of LED string1
2	Cathode of LED string2
3	Cathode of LED string3
4	NC
5	VLED
6	VLED
7	NC
8	Cathode of LED string4
9	Cathode of LED string5
10	Cathode of LED string6

4.4 LVDS INPUT SIGNAL SPECIFICATIONS



4.4.1 LVDS DATA MAPPING TABLE

LVDS Channel O0	LVDS output	D7	D6	D4	D3	D2	D1	D0
	Data order	OG0	OR5	OR4	OR3	OR2	OR1	OR0
LVDS Channel O1	LVDS output	D18	D15	D14	D13	D12	D9	D8
LVD3 Channel O1	Data order	OB1	OB0	OG5	OG4	OG3	OG2	OG1
LVDS Channel O2	LVDS output	D26	D25	D24	D22	D21	D20	D19
LVD3 Channel 02	Data order	DE	NA	NA	OB5	OB4	OB3	OB2
LVDS Channel O3	LVDS output	D23	D17	D16	D11	D10	D5	D27
LVD3 Charmer O3	Data order	NA	OB7	OB6	OG7	OG6	OR7	OR6

4.4.2 COLOR DATA INPUT ASSIGNMENT

The brightness of each primary color (red, green and blue) is based on the 8-bit gray scale data input for the color. The higher the binary input, the brighter the color. The table below provides the assignment of color versus data input.

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HIME	LINNOLIIX											D	at c	۰i~-	201										
					Re							Di		Sigr reer							Blu	10			
	Color	R7	R6	R5	R4		R2	R1	R0	G 7	G 6	G 5	G 4		G2	G1	G0	B 7	В6	В5			B2	B 1	B 0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Colors	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Red(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray	Red(2)	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Scale	:		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	Ξ.	Ė		:		:	:	:
Of	: Dad(050)	:	:	:	:	:	4	:	,	:	^	:	. 0	:	:	:	_	0		^		:	:	:	. ^
Red	Red(253)	1	1	1	1	1	1	0	1	0	0	0	:0	0	0	0	0	0	0	0	0	0	0	0	:0
	Red(254) Red(255)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	Green(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
Gray	Oreen(2)							.																	
Scale	•	:	:	:	:	:	:		:	:	:						:	:	:	:	:	:	:	:	:
Of	Green(253)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
Green	Green(254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	Ö	0	0	0	0	0	0	0
	Green(255)	0	0	0	Ö	0	0	0	0	1	1	1	1	1	1	1	1	0	0	Ö	0	0	0	0	0
	Blue(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
Cross	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
Gray	: ` ′	:	:	:	:	:	:	1:		8 :	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Scale Of	:	:	:	:	:	:				:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Blue	Blue(253)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
Diue	Blue(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

Note (1) 0: Low Level Voltage, 1: High Level Voltage

4.5 DISPLAY TIMING SPECIFICATIONS

The input signal timing specifications are shown as the following table and timing diagram.

Signal	Item	Symbol	Min.	Тур.	Max.	Unit	Note
	Frequency	Fc	62.9	75.4	95.6	MHz	-
	Period	Tc	10.5	13.2	15.9	ns	
LVDS	Input cycle to cycle jitter	T _{rcl}	-0.02*Tc		0.02*Tc	ns	(1)
Clock	Input Clock to data skew	TLVCCS	-0.02*Tc		0.02*Tc		(2)
CIOCK	Spread spectrum modulation range	Fclkin_mod	0.97*FC		1.03*FC	MHz	(3)
	Spread spectrum modulation frequency	F _{SSM}			100	KHz	(3)
\/awtiaal	Frame Rate	Fr	50	60	76	Hz	
Vertical	Total	Tv	780	806	900	Th	Tv=Tvd+Tvb
Display Term	Active Display	Tvd	_	768		Th	-
161111	Blank	Tvb	18	38	132	Th	-
Horizontal	Total	Th	1400	1560	1800	Tc	Th=Thd+Thb
Display	Active Display	Thd	_	1366		Tc	-
Term	Blank	Thb	40	194	434	Tc	-

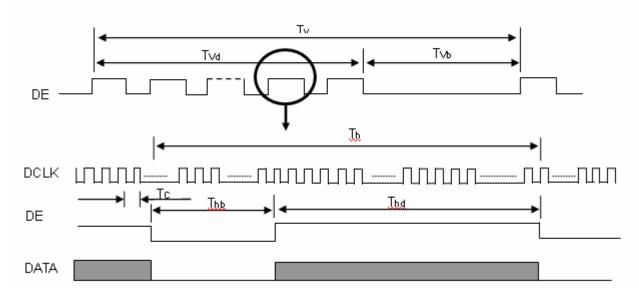
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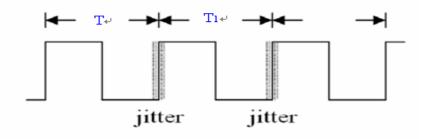
PRODUCT SPECIFICATION

Note: Because this module is operated by DE only mode, Hsync and Vsync input signals are ignored.

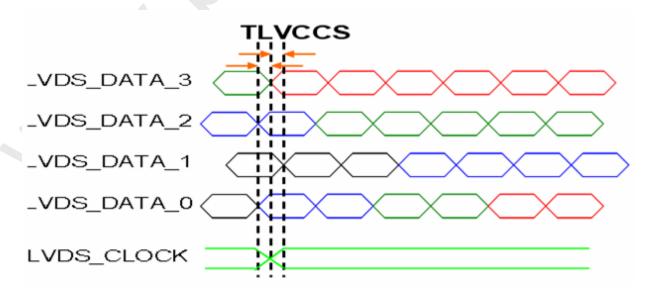
INPUT SIGNAL TIMING DIAGRAM



Note (1) The input clock cycle-to-cycle jitter is defined as below figures. Trcl = $IT_1 - TI$



Note (2) Input Clock to data skew is defined as below figures.

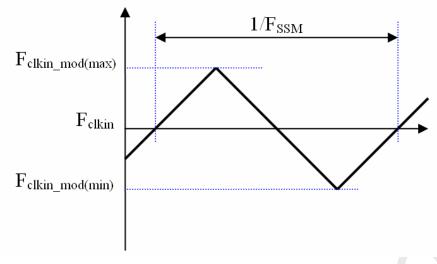


Note (3) The SSCG (Spread spectrum clock generator) is defined as below figures.

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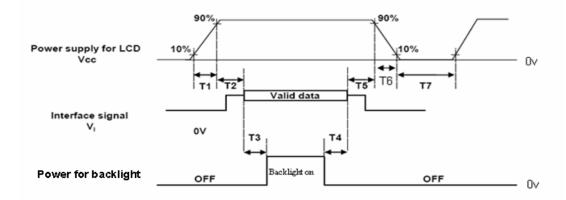


PRODUCT SPECIFICATION



4.6 POWER ON/OFF SEQUENCE

The power sequence specifications are shown as the following table and diagram.



Timing Specifications:

• .				
Parameters		Values		Units
Farameters	Min	Тур.	Max	Offics
T1	0.1	-	10	ms
T2	0	30	50	ms
T3	200	250	-	ms
T4	100	250	-	ms
T5	0	20	50	ms
T6	0.1	-	50	ms
T7	1000	-	-	ms

- Note (1) The supply voltage of the external system for the module input should be the same as the definition of Vcc.
- Note (2) When the backlight turns on before the LCD operation of the LCD turns off, the display may momentarily become abnormal screen.
- Note (3) In case of VCC = off level, please keep the level of input signals on the low or keep a high impedance.
- Note (4) T4 should be measured after the module has been fully discharged between power off and on period.

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Note (5) Interface signal shall not be kept at high impedance when the power is on.

Note (6) CMO won 't take any responsibility for the products which are damaged by the customers not following the Power Sequence.

Note (7) There might be slight electronic noise when LCD is turned off (even backlight unit is also off). To avoid this symptom, we suggest "Vcc falling timing" to follow "t6 spec".

5. OPTICAL CHARACTERISTICS

5.1 TEST CONDITIONS

Item	Symbol	Value	Unit			
Ambient Temperature	Та	25±2	°C			
Ambient Humidity	На	50±10	%RH			
Supply Voltage	V_{cc}	5	V			
Input Signal	According to typical v	alue in "3. ELECTRICAL (CHARACTERISTICS"			
LED Light Bar Input Current Per Input Pin	I _{PIN}	40 ± 1.2	mA _{DC}			
PWM Duty Ratio	D	100	%			
LED Light Bar Test Converter	TEST01001 T2-A1					

5.2 OPTICAL SPECIFICATIONS

The relative measurement methods of optical characteristics are shown in 5.2. The following items should be measured under the test conditions described in 5.1 and stable environment shown in Note (5).

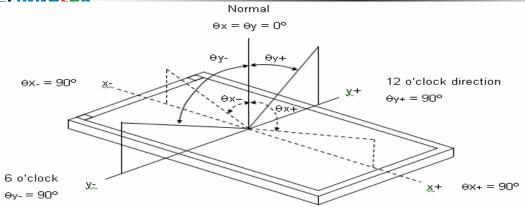
Iten	n	Symbol	Condition	Min.	Тур.	Max.	Unit	Note
	Red	Rx			0.641			
	Neu	Ry			0.338			
	Green	Gx			0.315			
Color Chromaticity	Oreen	Gy		Typ – 0.03	0.629	Typ +		(1), (5)
(CIE 1931)	Blue	Bx	0 -00 0 -00		0.159	0.03	_	(1), (3)
(ыйс	Ву	$\theta_x=0^\circ$, $\theta_Y=0^\circ$		0.059			
	White	Wx			0.313			
	vviile	Wy			0.329			
Center Lumina (Center of		L _C		200	250	-	cd/m ²	(4), (5)
Contrast	Ratio	CR		700	1000	-	-	(2), (5)
Respons	e Time	T_R	$\theta_x = 0^\circ, \ \theta_Y = 0^\circ$	-	1.5	4	ms	(3)
Troopono	C TITIE	T_F		-	3.5	6	1110	(0)
White Va	White Variation		θ_x =0°, θ_Y =0° SR-3A	0.70	0.75	-	-	(5), (6)
Viewing Angle	Horizontal	$\theta x - + \theta x +$	$CR \ge 10$	150	170	-	Deg.	(1), (5)
Viewing Angle	Vertical	θ y- + θ y+	SR-3A	140	160	-	Deg.	(1), (3)
Viewing Angle	Viewing Angle Horizontal		$CR \geq 5$	160	178		Deg.	(1), (5)
1.541119741910	Vertical	θ y- + θ y+	SR-3A	150	170		Dog.	(1), (0)

Note (1) Definition of Viewing Angle (θx , θy):

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Note (2) Definition of Contrast Ratio (CR):

The contrast ratio can be calculated by the following expression.

Contrast Ratio (CR) = L255 / L0

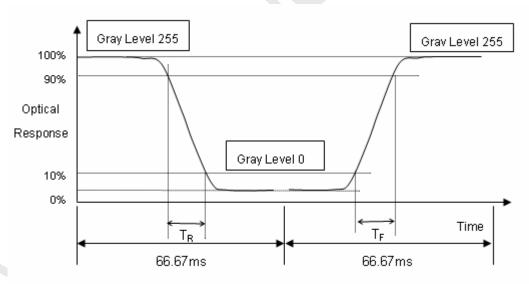
L255: Luminance of gray level 255

L 0: Luminance of gray level 0

CR = CR(5)

CR (X) is corresponding to the Contrast Ratio of the point X at Figure in Note (6).

Note (3) Definition of Response Time (T_R, T_F):



Note (4) Definition of Luminance of White (L_C):

Measure the luminance of gray level 255 at center point

 $L_{C} = L(5)$

L (x) is corresponding to the luminance of the point X at Figure in Note (6).

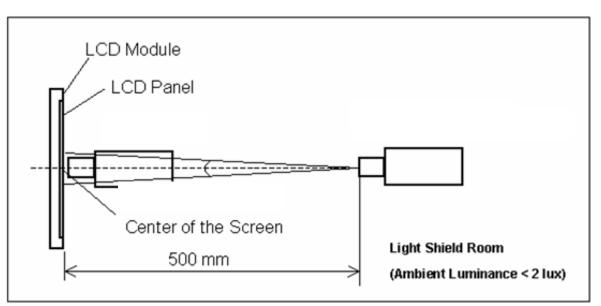
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Note (5) Measurement Setup:

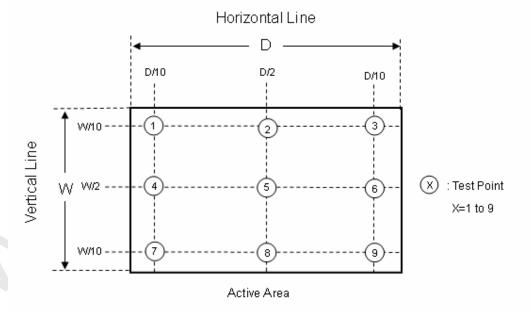
The LCD module should be stabilized at given temperature for 10 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 10 minutes in a windless room.



Note (6) Definition of White Variation (δW):

Measure the luminance of gray level 255 at 9 points

 $\delta W = Maximum [L (1) \sim L (9)] / Minimum [L (1) \sim L (9)]$



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6. RELIABILITY TEST ITEM

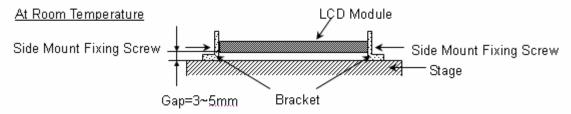
Items	Required Condition	Note
Temperature Humidity Bias (THB)	Ta= 50℃,80%RH, 240hours	
High Temperature Operation (HTO)	Ta= 50℃,240hours	
Low Temperature Operation (LTO)	Ta= 0°C , 240hours	
High Temperature Storage (HTS)	Ta= 60° C , 240hours	
Low Temperature Storage (LTS)	Ta= -25 $^{\circ}$ C , 240hours	
Vibration Test (Non-operation)	Acceleration: 1.5 G Wave: Half-sine Frequency: 10 - 300 Hz Sweep: 30 Minutes each Axis (X, Y, Z)	
Shock Test (Non-operation)	Acceleration: 50 G Wave: Half-sine Active Time: 11 ms Direction: ± X, ± Y, ± Z.(one time for each Axis)	
Thermal Shock Test (TST)	-[(-25°C 30min)→(65°C 30min)]/cycle · (Ramp≧20°C/min), 100cycles	
On/Off Test	25℃ ,On/10sec , Off /10sec , 30,000 cycles	
ESD (Electro Static Discharge)	Contact Discharge: ± 8KV, 150pF(330Ω) Air Discharge: ± 15KV, 150pF(330Ω)	
Altitude Test	Operation:10,000 ft / 24hours Non-Operation:30,000 ft / 24hours	

Note (1) criteria: Normal display image with no obvious non-uniformity and no line defect.

Note (2) Evaluation should be tested after storage at room temperature for more than two hour

Note (3) At testing Vibration and Shock, the fixture in holding the module has to be hard and rigid enough so that the module would not be twisted or bent by the fixture.

The fixing condition is shown as below:



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PRODUCT SPECIFICATION

7. PACKING

7.1 PACKING SPECIFICATIONS

(1) 10 LCD modules / 1 Box

(2) Box dimensions: 500(L) X285(W) X 435(H) mm

(3) Weight: approximately: 17.41kg (10 modules per box)

7.2 PACKING METHOD

(1) Carton Packing should have no failure in the following reliability test items.

Test Item	Test Conditions	Note
	ISTA STANDARD	
Vibration	1 . 5G, 1 0 t o 5 00 Hz , random ,30 mins for each	Non Operation
	axis X/Y/Z	
Dropping Test	1 Corner , 3 Edge, 6 Face, 60cm	Non Operation

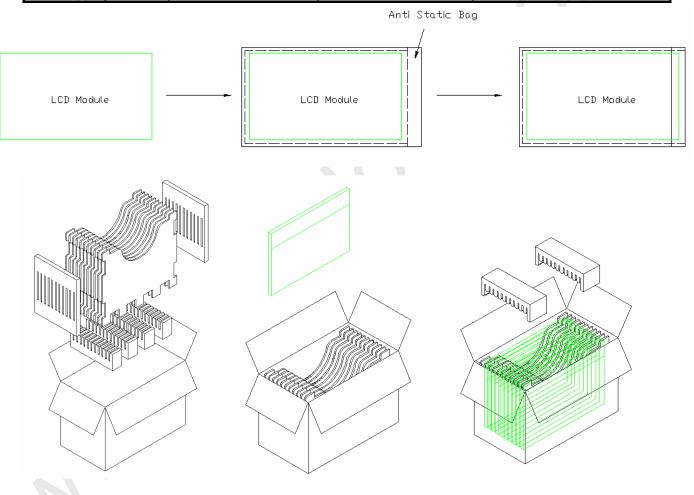


Figure. 7-1 Packing method

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7.3 PALLET

For Sea/Land Transportation (20ft/40ft Container)

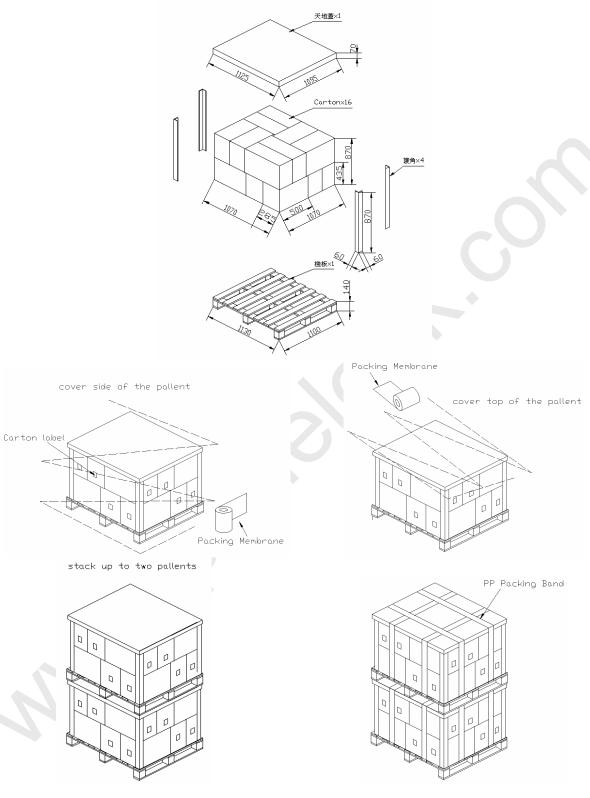


Figure. 7-2 Packing method

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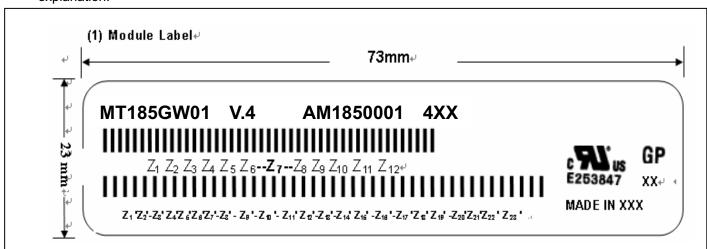
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8. CMI MODULE LABEL

The barcode nameplate is pasted on each module as illustration, and its definitions are as following explanation.



(a) Model Name: MT185GW01

((b) Version: V.4

(c) barcode definition:

Serial ID: Z₁ Z₂ Z₃ Z₄ Z₅ Z₆--**Z**₇--Z₆ Z₆ Z₁₀ Z₄₄ Z₄₂

OCHAH IB: 21 22 23	Serial 15. 21 22 23 24 25262728 29 210 2 11 2 12								
Code	Meaning	Description							
Z_1	internal use	-							
$Z_2Z_3Z_4$	Year ,Month ,Date	Year: 0~9, 2001=1, 2002=2, 2003=32010=0, 2011=1, 2012=2 Month: 1~12=1, 2, 3, ~, 9, A, B, C Day: 1~31=1, 2, 3, ~, 9, A, B, C, ~, W, X, Y, exclude I, O, and U.							
Z ₅	internal use	-							
Z ₆	internal use	-							
Z 7	Code of grade	G: 1, 2, 3, 5, E							
$Z_8Z_9Z_{10}Z_{11}Z_{12}$	Serial No	Module manufacture sequence no							

- (d) Serial ID II (INL internal use)
- (e) Factory ID:

Region	Factory ID	
H area of LH	00	
E area of LH	01	
CN	02	

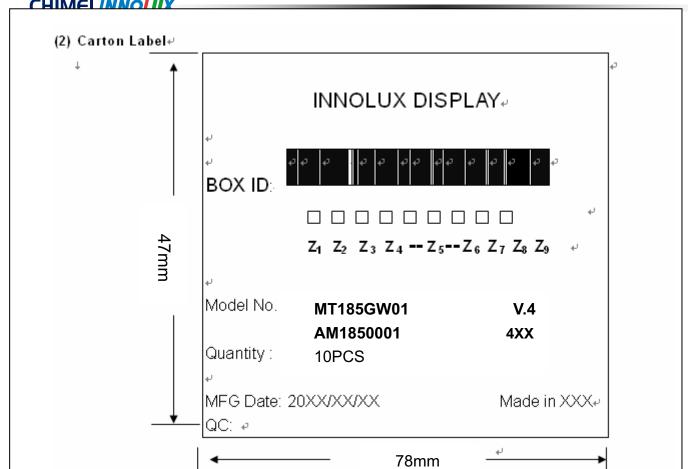
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奇美電子

Global LCD Panel Exchange Center

PRODUCT SPECIFICATION



(a) Model Name: MT185GW01

(b) Version: V.4

(c) Packing quantity: 10 pcs Serial ID: Z₁ Z₂ Z₃ Z₄₋₋₋ Z₅₋₋₋Z₆**Z**₇Z₈ Z₉

Code	Meaning	Description
Z_1	internal use	-
$Z_2Z_3Z_4$	Year ,Month ,Date	Year: 0~9, 2001=1, 2002=2, 2003=32010=0, 2011=1, 2012=2 Month: 1~12=1, 2, 3, ~, 9, A, B, C Day: 1~31=1, 2, 3, ~, 9, A, B, C, ~, W, X, Y, exclude I, O, and U.
Z ₅	Code of grade	G: 1, 2, 3, 5, E
Z ₆ Z ₇ Z ₈ Z ₉	Serial No	Module manufacture sequence no

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9.1 ASSEMBLY AND HANDLING PRECAUTIONS

- (1) Do not apply rough force such as bending or twisting to the module during assembly.
- (2) To assemble or install module into user's system can be only in clean working areas. The dust and oil may cause electrical short or worsen the polarizer.
- (3) It's not permitted to have pressure or impulse on the module because the LCD panel and Backlight will be damaged.
- (4) Always follow the correct power sequence when LCD module is connecting and operating. This can prevent damage to the CMOS LSI chips during latch-up.
- (5) Do not pull the I/F connector in or out while the module is operating.
- (6) Do not disassemble the module.
- (7) Use a soft dry cloth without chemicals for cleaning, because the surface of polarizer is very soft and easily scratched.
- (8) It is dangerous that moisture come into or contacted the LCD module, because moisture may damage LCD module when it is operating.
- (9) High temperature or humidity may reduce the performance of module. Please store LCD module within the specified storage conditions.
- (10)When ambient temperature is lower than 10°C may reduce the display quality. For example, the response time will become slowly.

9.2 STORAGE PRECAUTIONS

- (1) Do not leave the module in high temperature, and high humidity for a long time. It is highly recommended to store the module with temperature from 0° to 35° and relative humidity of less than 70%
- (2) Do not store the TFT LCD module in direct sunlight
- (3) The module should be stored in dark place. It is prohibited to apply sunlight or fluorescent light in storing

9.3 OPERATION PRECAUTIONS

(1) The LCD product should be operated under normal condition.

Normal condition is defined as below:

Temperature : 20±15°C Humidity: 65±20%

Display pattern: continually changing pattern(Not stationary)

(2) If the product will be used in extreme conditions such as high temperature, high humidity, high altitude, display pattern or operation time etc...It is strongly recommended to contact CMO for application engineering advice. Otherwise, Its reliability and function may not be guaranteed.

9.4 SAFETY PRECAUTIONS

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- (1) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, skin or clothes, it has to be washed away thoroughly with soap.
- (2) After the module's end of life, it is not harmful in case of normal operation and storage.

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9.5 SAFETY STANDARDS

The LCD module should be certified with safety regulations as follows:

- (1) UL60950-1 or updated standard.
- (2) IEC60950-1 or updated standard.

9.6 OTHER

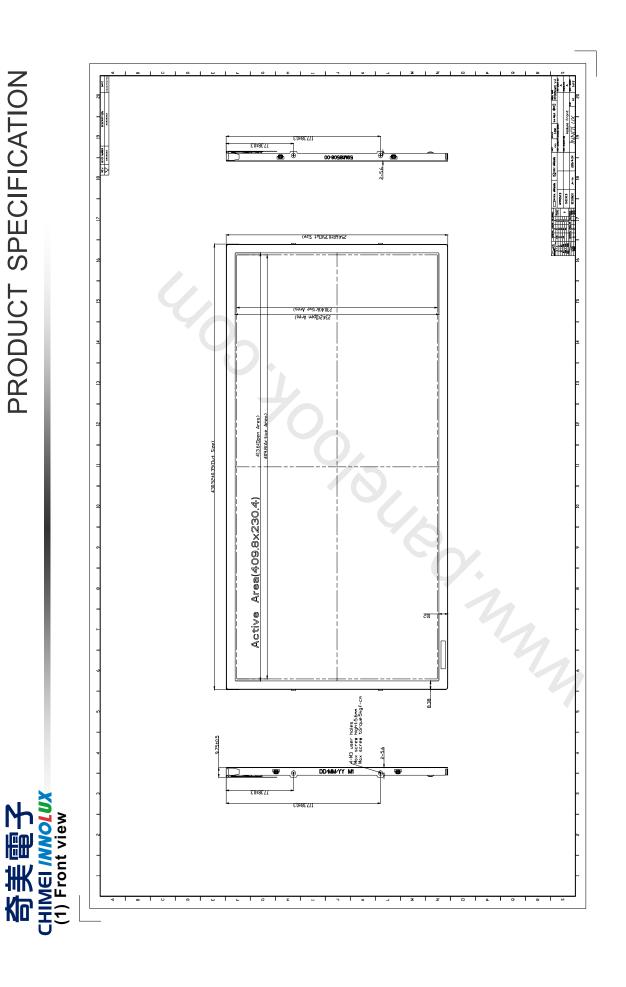
When fixed patterns are displayed for a long time, remnant image is likely to occur.

Appendix. OUTLINE DRAWING

(Refer to the next 3 pages)

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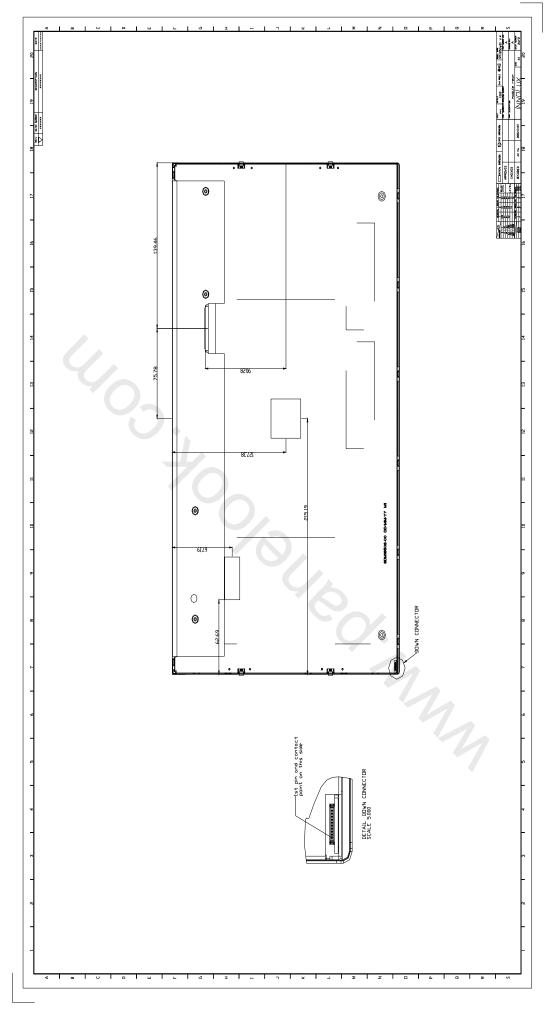


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屏庫:全球液晶屏交易中心



(2) Back view



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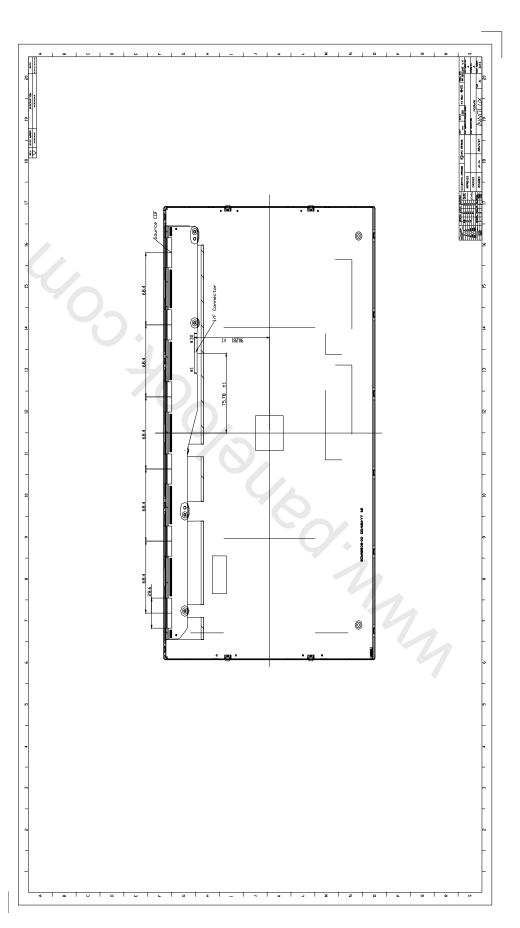
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